

SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEMS NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

No. SVE/16/152 Dated: 15-12-16

Subject: Ph.D. admissions 2016-17 (Even Semester).

The list of eligible/not eligible candidates is enclosed as Annexure-1.

- 1. All eligible candidates (listed in Annexure-1) are required to appear in the written test followed by interview on 26th December, 2016 in VLSI Design Lab of the school at 10:30 am onwards.
- 2. Question paper will consist of 50 objective type questions. Candidate has to obtain atleast 40% marks to qualify for the interview.
- 3. Written test will contain questions broadly from the following area: Electronics Devices, Digital Electronics, Microprocessor, Micro Electronics, Digital Signal Processing, VLS1 Design.

Note:-

- 1) All the applicants interested in Ph.D. admission are requested to ensure that they fulfil the qualifications as mentioned in Ph.D. prospectus.
- 2) Candidates are required to bring the supporting documents in original to prove their eligibility.
- 3) Selection for Ph.D. programme will be based mainly on the performance in the interview in respective area(s) of research/interest.

School of VLSI Design & Embedded Systems

Prof. (Dr.) R. K. Sharms

Copy to:

1. Dean (Academics)

2. Professor In-charge CCN for uploading the documents.

School of VLSI Design and Embedded Systems **National Institute of Technology Kurukshetra**



SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEMS NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

No. SVE/16/ Dated: 15-12-16

Ph.D. ADMISSIONS 2016-17 (EVEN SEMESTER)

Annexure-1

Sr. No.	Name of Candidate	Father's Name	Eligible/ Not Eligible
670	Seemanjali Sahoo	Prasanta Kumar Sahoo	Yes
671	Neha Pannu	Raj Kumar Pannu	Yes
672	C.Venkat Swamy	C.Bikashapathy	Yes
673	Ashish Singh	Akhilesh Kumar Singh	Yes
674	Vikas	Radhey Shyam	Yes
675	Ajay Beniwal	Roshan Kumar	Yes
676	Rahul Shandilya	Om Shivay	Not
677	Aditya Attri	Bahadur Singh Attri	Yes
678	Priti Gupta	Dinesh Chandra Gupta	Yes
679	Mandeep Singh	Santokh Singh	Yes
680	Neha Paras	Paras Ram	Yes
681	Yogita Singh	Nafe Singh	Yes
682	Seema	Hardyal Singh	Yes
683	Rekha Chaudhary	Maan Singh	Yes
684	Savita Lohat	Surya Parkash Lohat	Yes
685	Mansi	Rajesh Kumar	Yes
686	Monika	Surender Kumar	Yes
687	Jyoti Yadav	Ram Aytar	Yes
688	Shalu Rani	Baldev Kumar	Yes
689	Prasenjit Deb	Braja Gopal Deb	Yes

School of VI.SI Design & Embedded Systems

Prof. (Dr.) R. K. Sharma Coordinator

School of VLSI Design and Embedded Systems National Institute of Technology Kurukshetra