

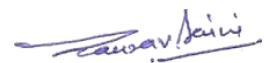
**SCHOOL OF VLSI DESIGN AND EMBEDDED SYSTEMS**  
**National Institute of Technology, Kurukshetra**  
**Schedule for the Class Test- 2<sup>nd</sup> (2020-21)**

**Dated: 23/11/2020**

<b>Date</b>	<b>Time</b>	<b>Subject Code</b>	<b>Faculty Members</b>
<b>Nov 26, 2020</b>	9.30AM to 10.20AM	MSV1E43	GV
	9.30AM to 10.20AM	MSV2E33	MK
	3.30PM to 4.20PM	MSV1C03	GS
	3.30PM to 4.20PM	MSV2C13	ABS
<b>Nov 27, 2020</b>	11.30AM to 12.20PM	MSV1C01/MSV2E41	SC
<b>Nov 28, 2020</b>	9.30AM to 10.20AM	MSV1C05/MSV2C15	RKS
	3.30PM to 4.20PM	MSV1E31/MSV2C11	ABS

**NOTE:**

- 1. All faculty members are requested to give extra 10 minutes to all students for submitting their answer sheet.**
- 2. Any discrepancy may please be brought to the notice of undersigned immediately.**



**Time Table I/C**  
**School of VLSI Design and Embedded Systems**

**NOTICE BOARD**

**Copy to:**

1. All Concerned through email
2. Coordinator, SVE for information