

NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,Embedded System Design
examinations
July-2020

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY IV Semester
,Embedded System Design examination of this Institute held in July-2020 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

| Sr. No. | Subjects | Code No. |
|---------|--------------|----------|
| 1 | Dissertation | SVE602P |

| Sr. No. | Roll No. | Name | F_Name | SGPA | CGPA |
|---------|----------|----------------|-------------------|--------|--------|
| 1 | 31811104 | GURALA SANDEEP | GURALA SUBBIREDDY | 9.0000 | 9.3226 |

Title of Dissertation : **Post Silicon CPU SOC Validation and automation Tools**

| | | | | | |
|---|----------|---------------|---------------|---------|--------|
| 2 | 31811105 | K. GARANKUMAR | K CHENCHIAIAH | 10.0000 | 9.5968 |
|---|----------|---------------|---------------|---------|--------|

Title of Dissertation : **Post Silicon 10th Generation CPU Validation and Debug System**

| | | | | | |
|---|----------|-------------|---------------|--------|--------|
| 3 | 31811110 | AMAN SAXENA | MUKESH SAXENA | 9.0000 | 8.8871 |
|---|----------|-------------|---------------|--------|--------|

Title of Dissertation : **FPGA Based Object Parameter Detection for Embedded Vision Applications**

| | | | | | |
|---|----------|--------------|-----------------------|--------|--------|
| 4 | 31811111 | SHWETA DUBEY | CHANDRA PRAKASH DUBEY | 8.0000 | 8.5161 |
|---|----------|--------------|-----------------------|--------|--------|

Title of Dissertation : **Design and Execution of Enhanced Carry Increment adder using Carry skip Adder**

| | | | | | |
|---|----------|----------------|---------------------|--------|--------|
| 5 | 31811113 | GUDLA SRIKANTH | GUDLA SRINIVASA RAO | 9.0000 | 9.4516 |
|---|----------|----------------|---------------------|--------|--------|

Title of Dissertation : **Interconnect Delay Optimization in Fundamental Unit Block of Microprocessor Core**

| | | | | | |
|---|----------|-------------------------------|----------------|--------|--------|
| 6 | 31811114 | KOVVURU THIRUMANJUNATHA REDDY | K ANJANA REDDY | 9.0000 | 8.4839 |
|---|----------|-------------------------------|----------------|--------|--------|

Title of Dissertation : **AUTOSAR Development on Service Discovery Module**

05 AUG 2020

MB. Raut 04/08/2020

04/08/2020

NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,Embedded System Design
examinations
July-2020

7 31811117 GEETANJALI ROHILLA RAVINDRA KUMAR 10.0000 9.5645

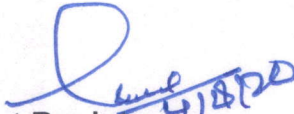
Title of Dissertation : **Functional Verification of Testchips**

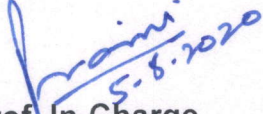
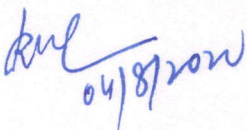
8 31811118 VIKAS SHARMA SUSHIL KUMAR 10.0000 9.1452

Title of Dissertation : **Performance Improvement of a Digital thermal Sensor w.r.t Variations in
Reference Voltag**

**All efforts have been made to publish this result after checking the entries properly.
However, the result can stand revised in case some discrepancy is observed**

Date : 05 AUG 2020
Kurukshetra


Joint Registrar
(Examinations)
MB. Raut 04/08/2020


Prof. In-Charge
(Examinations)

04/08/2020