

SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEMS
NATIONAL INSTITUTE OF TECHNOLOGY
KURUKSHETRA

No. SVE/19/ 135 -

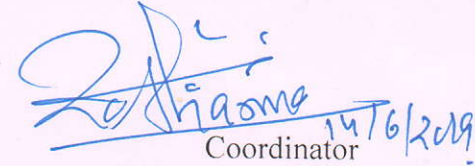
Date: 14-06-2019

Subject: Ph.D Admission test 2019-20

Ph.D. admission test was held on 14-06-2019 at 10 am. Following candidates qualified the test:

Sr. No.	Application No. with Date	Candidate Name	M/F	Father/ Husband Name	Cat.	Eligible / Provisinally Eligible	Part Time / Full Time
1.	VLSI-01, 27.05.2019	Sapna Singh	F	Sh. Ramjeet Singh	SC	E	Full Time
2.	VLSI-05, 29.05.2019	Prakash Chandra Joshi	M	Sh. Ramesh Chandra Joshi	Gen	E	Part Time
3.	VLSI-08, 30.05.2019	Ramesh Kumar	M	Sh. Jai Narayan Singh	Gen	PE	Full Time
4.	VLSI-13, 03.06.2019	Neelam Kumari	F	Sh. Pyarey Lal	OBC	PE	Part Time
5.	VLSI-18, 03.06.2019	Arashpreet Kaur	F	Sh. Sukhjinder Singh	OBC (ncl)	PE	Full Time
6.	VLSI-20, 03.06.2019	Ashirbad Khuntia	M	Sh. Bijay Kumar Khuntia	Gen	E	Full Time

All qualified candidates, reporting for interview on 19-06-2019 are required to present PPTs related to their research interest.


Coordinator 14/6/2019

School of VLSI Design & Embedded Systems

Dean (Academic)

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