


SCHOOL OF VLSI DESIGN AND EMBEDDED SYSTEMS
NATIONAL INSTITUTE OF TECHNOLOGY, KURUKSHETRA

No SVE/2020/535

Date: 24-10-2020

Following candidates have qualified in the written test held on 21/10/2020 for admission in the Ph.D. program of School of VLSI design and Embedded Systems at NIT Kurukshetra. The qualified candidates are required to appear in the interview to be held on 28/10/2020 at 2:00 pm. The guidelines for the interview are enclosed herewith.

S.No.	Name	Father's Name	Application No.	Part Time/Full Time
1.	Vineet Jaiswal	Naresh Chandra Jaiswal	VLSI-64	Full Time
2.	Jaya Nidhi Vashishtha	Radhey Shyam Sharma	VLSI-348	Part Time
3.	Mayurika Saxena	Ashok Kumar Saxena	VLSI-576	Part Time


24/10/2020
Coordinator

School of VLSI Design and Embedded Systems

Encl.: Ph.D. Interview Guidelines

CCN for displaying on website


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No SVE/2020/

Date:21-10-2020

PhD interview guidelines

1. List of written test qualified candidates will be uploaded on Institute website latest by **25-10-2020** (Morning).
2. The written test qualified candidates would be required to submit an undertaking and scanned copy of all original certificates by **27-10-2020 (Morning)** to SVE office (email id: nitkkr.sve@gmail.com).
3. OBC (Non Creamy Layer) certificate is valid only if issued on or after 01-04-2020. M.Tech. certificate is valid only if issued before last date of application. Part time candidates will have to submit the NOC from host institute at the time of interview.
4. Only written test qualified candidates will be interviewed **through online means (Google meet)** by DRC on 28-10-2020 at 2:00 pm onwards. Link for the same would be sent in due course of time. (Presentation will include Maximum 10 slides on proposed research/area of interest)


21/10/2020
Coordinator, SVE

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