

NATIONAL INSTITUTE OF TECHNOLOGY, KURUKSHETRA

Re-Notification for Result of MASTER OF TECHNOLOGY I Semester ,VLSI Design examinations
Nov./Dec., 2017

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY I Semester ,VLSI Design examination of this Institute held in Nov./Dec., 2017 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code No.
1	Digital IC Design	SVE 501T
2	Analog IC Design	SVE 503T
3	Hardware Description Languages and FPGA Based Design	SVE 505T
4	Embedded System Fundamentals	SVE 511T
5	Hardware Algorithms for Computer Arithmetic	SVE 519T
6	VLSI Design Lab-I	SVE 507P
7	Seminar	SVE 509P

Sr. No.	Roll No.	Name	F_Name	SGPA
1	31611231	SACHINDRA BHARTI	SH.HARDAY NARAYAN BHAGAT	6.3889

All efforts have been made to publish this result after checking the enteries properly.
However, the result can stand revised in case some discrepancy is observed

Date : 25 JUL 2018
Kurukshetra

Ravi Kant
27/7/18
Prof. In-Charge
(Examinations)

Mr. Ravi Kant
27/07/18

Ravi Kant
27.07.18 27/7/18