

NATIONAL INSTITUTE OF TECHNOLOGY, KURUKSHETRA

Re-Appear Notification for Result of MASTER OF TECHNOLOGY II Semester ,VLSI Design examinations
Nov/Dec -2019

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY II Semester ,VLSI Design
examination of this Institute held in Nov/Dec -2019 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

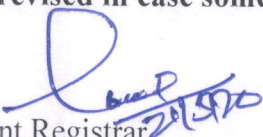
RL(A) For Result Late (Award)

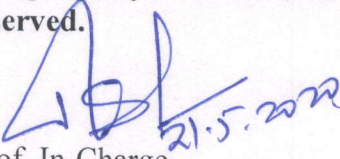
Sr. No.	Subjects	Code No.
1	Mixed Signal IC Design	SVE-500T
2	Modeling of semiconductor devices	SVE-502T
3	Low Power VLSI Design	SVE-510T
4	RF Micro-electronics	SVE-514T
5	VLSI Testing and verification	SVE-520T
6	VLSI Design Lab-II	SVE 504P
7	Seminar	SVE506P

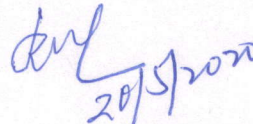
Sr. No.	Roll No.	Name	F_Name	SGPA	CGPA
1	31711202	DHEERAJ PUNIA	AJIT SINGH PUNIA	7.1667	7.2500
2	31711226	SWATI SHARMA	RAKESHCHANDRA SHARMA	6.1667	5.9167

**All efforts have been made to publish this result after checking the entries properly. However, the
result can stand revised in case some discrepancy is observed.**

Date : 21 MAY 2020
Kurukshetra


Joint Registrar
(Examinations)
MB. Rat 20/5/2020


Prof. In-Charge
(Examinations)
21.5.2020


20/5/2020