

# NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,VLSI Design examinations  
October-2021

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY IV Semester ,VLSI  
Design examination of this Institute held in October-2021 is declared as under:-

**Note: SGPA shown means Pass and "R" means Reappear**

**RL(A) For Result Late (Award)**

Sr. No.	Subjects	Code No.
1	Dissertation	MSV1D10

Sr. No.	Roll No.	Name	F_Name	SGPA	CGPA
1	31911202	AKANKSHA THAKUR	MUNNALAL THAKUR	8.0000	8.1583

Title of Dissertation : **Study of tunnel magneto-resistance in Junctionless Magnetoresistive Device based on passivated zigzag gallium nitride nano-ribbon**

2	31911203	SIMRAN SANJAYKUMAR BHAISARE	SANJAYKUMAR SITARAMJI EHAISARE	9.0000	9.0469
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Title of Dissertation : **RTL Signoff Challenges and solutions for SoC Design**

3	31911216	SAVERA YADAV	MADAN MOHAN SINGH YADAV	9.0000	8.4531
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Title of Dissertation : **Study of Electronic and Optical properties of Ca<sub>2</sub>TiSi(O<sub>1-x</sub>S<sub>x</sub>)<sub>6</sub> for photovoltaic applications**

4	31911218	SAURAV KUMAR	SHAMBHU CHAUDHARY	9.0000	9.0625
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Title of Dissertation : **Methodology to Handle the Challenges in Clock Domain Crossings for LPDDR4 PHY**

5	31911219	BHANU PRATAP SINGH	SUBODH KUMAR	9.0000	9.0625
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Title of Dissertation : **Formal Verification of Debug Subsystem in SOC**

6	31911222	SHREYANSH AGRAWAL	SUNIL AGRAWAL	9.0000	8.3281
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Title of Dissertation : **Formal Verification of Hardware Neural Network Design of Car Collision Avoidance System**

7	31911225	SONU KUMAR	BIMAL PRASAD SINGH	8.0000	7.8281
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Title of Dissertation : **Analysis and Design of Low Power, Low Noise SAR ADC-A REVIEW**

26 OCT 2021

Rat 27/10/2021

27/10/21

# NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,VLSI Design examinations  
October-2021

8      31911226      SUSHANT      NARESH KUMAR      9.0000      9.0781

Title of Dissertation :      **RDL Routing Impact in Optimization of Physical Design Statistics**

9      31911227      ASHISH SARSWAT      RAMASHANKAR SARSWAT      8.0000      8.5781

Title of Dissertation :      **Design of Branch Prediction Unit for RISC-V Processor**

**All efforts have been made to publish this result after checking the entries properly.  
However, the result can stand revised in case some discrepancy is observed**

Date : 26 OCT 2021  
Kurukshetra

*Prakash*  
Prof. In-Charge  
(Examinations)  
*Prakash* *Ratna* 27/10/2021  
*Himani* *Kul* 27/10/21