

NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,VLSI Design examinations
September-2018

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY IV Semester ,VLSI
Design examination of this Institute held in September-2018 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code No.
1	Dissertation	SVE602P

Sr. No.	Roll No.	Name	F_Name	SGPA	CGPA
1	31611222	SAPNA DEVI	SH.BUA DITTA	9.0000	8.3226

Title Of Dissertation : **Design of High Speed Low Power SAR Analog to Digital Converter in 16 nm FinFET CMOS**

2	31611226	VAISHALI	SH.LAJPAT RAI	10.0000	8.5484
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Title Of Dissertation : **Implementation of CMOS Phase Locked Loop (PLL) in 180nm**

**All efforts have been made to publish this result after checking the entries properly.
However, the result can stand revised in case some discrepancy is observed**

Date : **25 SEP 2018**
Kurukshetra

Ravi Kaur
Prof. In-Charge
(Examinations)

Ms. Rishi 17/09/18

18.09. 18/9/18