

# NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,VLSI Design examinations  
August-2019

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY IV Semester ,VLSI Design examination of this Institute held in August-2019 is declared as under:-

**Note: SGPA shown means Pass and "R" means Reappear**

**RL(A) For Result Late (Award)**

Sr. No.	Subjects	Code No.
1	Dissertation	SVE602P

Sr. No.	Roll No.	Name	F_Name	SGPA	CGPA
1	31705121	SONAKSHI BENIWAL	SUBHASH CHANDRA BENIWAL	9.0000	8.5968 ✓

Title of Dissertation : **Performance enhancement of DG-TFET using channel and gate engineering**

2	31709101	ABHISHEK ANANDA	SHAIENDRA KUMAR SINHA	10.0000	8.1452 ✓
---	----------	-----------------	-----------------------	---------	----------

Title of Dissertation : **Design and Simulation of Dual Metal Double Gate Drain Pocket TFET**

3	31709107	LOKENDER VASHIST	BHAI RAM VASHIST	8.0000	8.1129 ✓
---	----------	------------------	------------------	--------	----------

Title of Dissertation : **Real Time Canny Edge Detection Using Zybo Board**

4	31711203	AKASH JAISWAL	AKHILESH JAISWAL	8.0000	8.3871 ✓
---	----------	---------------	------------------	--------	----------

Title of Dissertation : **Physical Implementation & Power/Delay Optimization of a Subsystem**

5	31711213	NIPENDRA SINGH	SHIVENDRA SINGH	8.0000	7.9677 ✓
---	----------	----------------	-----------------	--------	----------

Title of Dissertation : **Induced Magnetization in CNTs on Adsorbing Transition Metals**

6	31711214	POLU SUDHEER KUMAR REDDY	POLU SREENEVASA REDDY	8.0000	8.5806 ✓
---	----------	--------------------------	-----------------------	--------	----------

Title of Dissertation : **Study of Reversible Logic for Low Power Applications**

7	31711219	ACHAL GARG	BAL KISHAN GARG	8.0000	8.6613 ✓
---	----------	------------	-----------------	--------	----------

Title of Dissertation : **Enhanced Absorption in MoS<sub>2</sub>/Hg<sub>0.33</sub>Cd<sub>0.66</sub>Te Heterostructure for Application in Solar Cell Absorbers**

# NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,VLSI Design examinations  
August-2019

8

31711222

RAJAT KUMAR MAHESHWARI KRISHAN MOHAN GANDHI


8.0000 8.4677

Title of Dissertation :

**Design of GMII Transparent Interface Based on Ethernet IEEE 802.3 Standard**

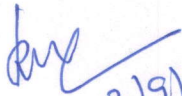
**All efforts have been made to publish this result after checking the entries properly.  
However, the result can stand revised in case some discrepancy is observed**

Date : 25 SEP 2019  
Kurukshetra

  
Deputy Registrar  
( Examinations)

Prof. In-Charge  
(Examinations)

MB.   
23/9/19

  
23/9/19