

NATIONAL INSTITUTE OF TECHNOLOGY, KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY II Semester ,Embedded System Design
examinations
May/June, 2017

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY II Semester
,Embedded System Design examination of this Institute held in May/June, 2017 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

| Sr. No. | Subjects | Code No. |
|---------|-----------------------------|----------|
| 1 | Mixed Signal IC Design | SVE-500T |
| 2 | Low Power VLSI Design | SVE-510T |
| 3 | Real Time Systems | SVE-512T |
| 4 | RF Micro-electronics | SVE-514T |
| 5 | Hardware Software Co-design | SVE-532T |
| 6 | Design Lab | SVE534P |
| 7 | Seminar | SVE536P |

| Sr. No. | Roll No. | Name | F_Name | SGPA | CGPA |
|---------|----------|-------------|----------------------|--------|--------|
| 1 | 31611112 | JYOTI RANI | SH.ASHOK KUMAR | 7.8333 | 7.4444 |
| 2 | 31611120 | SONAL GUPTA | SH.SUNIL KUMAR GUPTA | 8.2778 | 7.6944 |

All efforts have been made to publish this result after checking the enteries properly.
However,the result can stand revised in case some discrepancy is observed

Date : 08.09.2017
Kurukshetra

Ravi Kant
Prof. In-Charge
(Examinations)

MB Raut 08/09/17
Ravi