

NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Revised Notification for Result of MASTER OF TECHNOLOGY II Semester ,VLSI Design examinations
May/June, 2017

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY II Semester ,VLSI
Design examination of this Institute held in May/June, 2017 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code No.
1	Mixed Signal IC Design	SVE-500T
2	Modeling of semiconductor devices	SVE-502T
3	Low Power VLSI Design	SVE-510T
4	RF Micro-electronics	SVE-514T
5	VLSI Testing and verification	SVE-520T
6	VLSI Design Lab-II	SVE 504P
7	Seminar	SVE506P

Sr. No.	Roll No.	Name	F_Name	SGPA	CGPA
1	31611234	AVINASH KRANTI	SH.ASHOK KUMAR SINGH	6.5000	6.3889
2	31611237	SHEETAL RANI	SH.ROSHAN LAL SAINI	6.3333	6.2500

All efforts have been made to publish this result after checking the entries properly.
However, the result can stand revised in case some discrepancy is observed

Date: 17.04.2018
Kurukshetra

Ravi Kant
Prof. In-Charge
(Examinations)
20/4/18
20/04/18
ave