

National Institute of Technology Kurukshetra
(School of VLSI Design and Embedded Systems)

Schedule of End Semester Theory Examination
EVEN SEMESTER 2020-2021

Dated: 06/05/2021

| Date | Time | Course Code | Subject Name | Branch | Course co-ordinator/ Examiner |
|--------------------|--------------------|-----------------------------|-----------------------------------|-------------------|--|
| 11 May 2021 | 10:00AM to 12:00PM | MSV1C02 | Modeling of Semiconductor Devices | VLSI | AN |
| 13 May 2021 | 10:00AM to 12:00PM | MSV1C04/ MSV2E42 | Low Power VLSI Design | VLSI/ Embedded | RKS |
| 15 May 2021 | 10:00AM to 12:00PM | MSV1E36/ MSV2C22 | Real Time Systems | VLSI/ Embedded | MND |
| 17 May 2021 | 10:00AM to 12:00PM | MSV1E40 | VLSI Physical Design | VLSI | DKS |
| 19 May 2021 | 10:00AM to 12:00PM | MSV1E42 | VLSI Testing and Verification | VLSI | AS |
| 17 May 2021 | 10:00AM to 12:00PM | MSV2C24 | Hardware Software Co-design | Embedded | GV |
| 19 May 2021 | 10:00AM to 12:00PM | MSV2E34 | Wireless Sensor Networks | Embedded | SSC |
| 22 May 2021 | 10:00AM to 12:00PM | MSV2O72 | Mechatronics | Embedded | MPRP |


Co-ordinator VLSI


Time Table I/C
School of VLSI Design and Embedded Systems

NOTICE BOARD

Copy to:

1. All Concerned through email
2. Prof. in-charge exam
3. DR Academic