

National Institute of Technology Kurukshetra
(School of VLSI Design and Embedded Systems)

Schedule of End Semester Theory Examination
EVEN SEMESTER 2021-2022

No. SVE/2022/95

Dated: 06/05/2022

Venue: ECE Lecture Halls (L1, L5, L6)

Timings: Morning Session (09:30 AM to 12:30 PM)

Date of Exam	Course Code	Subject Name	Branch	Course co-ordinator/ Invigilator
17 May 2022	MSV1C04/ MSV2E42	Low Power VLSI Design	VLSI/ Embedded	MUK
23 May 2022	MSV1E42	VLSI Testing and Verification	VLSI	GS
	MSV2E34	Wireless Sensor Networks	Embedded	VG
27 May 2022	MSV1C02	Modeling of Semiconductor Devices	VLSI	AN
	MSV2E40	Computer Based Control Systems	Embedded	MPRP
31 May 2022	MSV1E40	VLSI Physical Design	VLSI	DKS
	MSV2C24	Hardware Software Co-design	Embedded	GV
04 June 2022	MSV1E36/ MSV2C22	Real Time Systems	VLSI/ Embedded	MHS

NOTE: (1) Any discrepancy may please be brought to the notice of undersigned immediately.

(2) Rooms will be shared with M.Tech (Computer Engg.) and M.Tech CO (Cyber Security)

(3) Dr. Ashutosh Nandi will work as Exam. Coordinator to take care of Exam related issues.

(4) As per the notification dated No. Acad./2022/665 dated 01/05/2022 **there will be simultaneous online exam** for the students who have been diagnosed as COVID-19 positive and are advised to self-quarantine themselves by the Medical officer of the Institute/Govt. hospital. In this regard, all such students will submit their requests along with recommendation / endorsement on corona Test Report by SMO / MO of Institute Health Center in the office of the respective HoDs / School Coordinators.


Co-ordinator

School of VLSI Design and Embedded Systems

Copy to:

1. All Concerned (Faculty and students) through email
2. Prof. I/C CCN with a request to upload the notice on the institute website.
3. Prof. in-charge Exam
4. DR (Acad.)
5. Dept. Notice Board
6. HoD Computer Engg.