

NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,VLSI Design examinations
October-2020

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY IV Semester ,VLSI
Design examination of this Institute held in October-2020 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

| Sr. No. | Subjects | Code No. |
|---------|--------------|----------|
| 1 | Dissertation | SVE602P |

| Sr. No. | Roll No. | Name | F_Name | SGPA | CGPA |
|---------|----------|--------------------------------|-----------------------|--------|--------|
| 1 | 31804316 | EDAMA VENKATASURENDRA KUMAR | EDAMA VENKATASUBBAIAH | 8.0000 | 8.3387 |

Title of Dissertation : **Sequential Analysis Driven Reset Optimization Using Power Pro Tool** ✓

| | | | | | |
|---|----------|------------------|-------------------|--------|--------|
| 2 | 31805105 | DIGVIJAY REDDY P | P HANUMANTH REDDY | 9.0000 | 8.1613 |
|---|----------|------------------|-------------------|--------|--------|

Title of Dissertation : **Optimized Shell Models for ATPG** ✓

| | | | | | |
|---|----------|---------------------|-----------------------|--------|--------|
| 3 | 31805115 | MANNE ARCHITH REDDY | MANNE PRABHAKAR REDDY | 9.0000 | 8.6290 |
|---|----------|---------------------|-----------------------|--------|--------|

Title of Dissertation : **Early Prototyping of PCIe Using Virtual Devices** ✓

| | | | | | |
|---|----------|-------------|---------------|--------|--------|
| 4 | 31811207 | RAVURI SIVA | RAVURI RAMANA | 9.0000 | 8.1613 |
|---|----------|-------------|---------------|--------|--------|

Title of Dissertation : **Constraints Verification and Certification Flow Using Excellicon for High Performance MCU's**

| | | | | | |
|---|----------|-------------------------------|-------------------|--------|--------|
| 5 | 31811208 | THOLLIKONDA SARATH CHANDRA | T RAMACHANDRA RAO | 9.0000 | 8.4355 |
|---|----------|-------------------------------|-------------------|--------|--------|

Title of Dissertation : **Unified Flow for Scalable Emulation and FPGA-Prototyping for Multi-Market Segments**

| | | | | | |
|---|----------|------------------------|---------------|--------|--------|
| 6 | 31811211 | ADENNAGARI SUBRAMANYAM | A.SUBBARAYUDU | 9.0000 | 8.7097 |
|---|----------|------------------------|---------------|--------|--------|

Title of Dissertation : **GNRFET Based Mixed Logic Line Decoder in 2 to 4 and 4 to 16 Mode with 22 nm Technology**

| | | | | | |
|---|----------|---------------------------------|------------------|--------|--------|
| 7 | 31811213 | VENGALAM THIRUMALESWAR REDDY | V SRINIVAS REDDY | 8.0000 | 8.3548 |
|---|----------|---------------------------------|------------------|--------|--------|

Title of Dissertation : **Hard Macro Implementation on Lower Technology Node**

09 NOV 2020

MB.
5-11-20

[Signature]

[Signature]
06/11/2020

NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,VLSI Design examinations
October-2020

8 31811214 KARAN SHARMA GOPAL KRISHAN 9.0000 8.1774

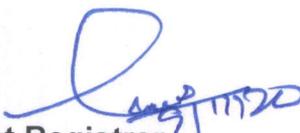
Title of Dissertation : **Optical and Electronic Properties of WS₂/GaS Hetero Structure**

9 31811224 RALLAPALLI JAYAKUMAR R.V.S.SATYANARAYANA 8.0000 8.0161

Title of Dissertation : **Study of Macroinstruction Execution in x86 Architecture Using Microcode Branch Coverage Metrics**

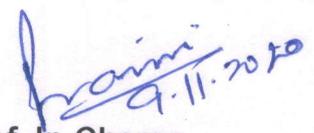
**All efforts have been made to publish this result after checking the entries properly.
However, the result can stand revised in case some discrepancy is observed**

Date : 09 NOV 2020
Kurukshetra


Joint Registrar
(Examinations)

MB.
5-11-20

05/11/2020


Prof. In-Charge
(Examinations)


06/11/2020