LIST OF THE Ph.D. APPLICANTS, ELIGIBLE FOR WRITTEN TEST TO BE HELD ON 13.08.2018
SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEM

Sr. No.	Application No.	Name of Candidate	Category	Specialization for which Applied	GATE Qualified (Yes/No)	Score	JRF/NET etc. details	Candidates Satisfies eligibility Criterion % of Marks/CGPA	Yes/No	If candidate is NOT ELIGIBLE, state reasons
1	SV-1	ASHA	GENERAL	VLSI DESIGN	YES	382	NET-65.14	75.94	YES	ELIGIBLE
2	SV-2	BALLA PAVAN KUMAR	OBC	EMBEDDED SYSTEMS	YES	388	-	82.1	YES	ELIGIBLE
3	SV-3	GAURAV YADAV	OBC	VLSI DESIGN	YES	489	-	7.17- 3RD SEMESTER	NO	4TH SEM. RESULT NOT YET DECLARE & CANDIDATE IS STUDENT AT NITKKR (M.TECH (SVE)
4	SV-4	VIVEK PRATAP SINGH	PH	VLSI DESIGN/EMBEDDED SYSTEM DESIGN	NO	-	-	7.48	NO	GATE NOT QUALIFIED, SO THAT NOT ELIGIBLE
5	SV-5	NEHA SAINI	OBC	VLSI DESIGN	NO	-	-	4TH SEM. R A	NO	GATE NOT QUALIFIED, 4TH SEMESTER RESULT NOT ATTACHED
6	SV-6	SAPNA SINGH	SC	VLSI DESIGN	YES	268	-	7.7258	YES	ELIGIBLE
7	SV-7	ANIL KUMAR SINGH	OBC	ESD AND VLSI DESIGN	YES	584	-	76.6, CGPA- 8.4156	YES	ELIGIBLE
8	SV-8	GOURAV KUMAR SONI	OBC	ESD AND VLSI DESIGN	YES	576	-	7.3871	YES	ELIGIBLE

Note: 1. All the eligible candidates must have acquired the qualifying degree by 23.07.2018

2. In the absence of valid category certificate, the candidate will be considered against open/General category

Sd/-

DEAN (ACADEMIC)