## NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Semester ,VLSI Design examinations November-2021

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY IV Semester ,VLSI
Design examination of this Institute held in November-2021 is declared as under:-

## Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.
Subjects
Dissertation

| Sr. No. | Roll No. |
| :---: | ---: |
| 1 | 31911228 |
|  |  |
|  |  |

Code No.
MSV1D10

GPA
F_Name
8.0000

GPA
8.9063

Formal Techniques Used in Verification of Generic Blocks in Computer Architecture

All efforts have been made to publish this result after checking the entries properly.
However, the result can stand revised in case some discrepancy is observed

Date: 23 NOV 2021
Kurukshetra

## NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

Notification for Result of MASTER OF TECHNOLOGY IV Seriiester ,VLSI Design examinations October-2021

The Result of the following candidates who appeared in MASTER OF TECHNOLOGY IV Semester ,VLS Design examination of this Institute held in October-2021 is declared as under:-

## Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

## Sr. No. Subjects

1 Dissertation

Code No.
MSV1D10

F_Name
MATHURTHI MALLESWARA RAD

| SGPA | GPA |
| :---: | :---: |
| 8.0000 | 8.8438 |

Title of Dissertation: $\quad$| Functional Verification of Hard Macro PPs (HM) with Automation of System-on- |
| :--- |



> Design and Analysis of Low Power SГ:AM cell



All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

## 23 NOV 2n71

Kurukshetra



