

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester **School of VLSI Design and Embedded System (VLSI Design)** examinations **August-2022**

The Result of the following candidates who appeared in Master of Technology IV Semester **School of VLSI Design and Embedded System (VLSI Design)** examination of this Institute held in **August-2022** is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.		
1	DISSERTATION		MSV1D10		
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018202	Anil Kumar	Dalbir Singh	8.0000	8.8750
Title of Dissertation :		Validation Of DTS Thermal For Power Management in Server SOC			
2	32018212	Himani Agrawal	Pradeep Kumar Gupta	9.0000	8.9219
Title of Dissertation :		Timing Closure and Performance Optimization In Advanced SoC Design			
3	32018213	Hrishikesh Saikia	Pankaj Saikia	8.0000	8.7500
Title of Dissertation :		Design And Analysis Of Power Delivery Networks For Optimizing Ir-Drop			
4	32018218	Mitesh Kumar	Rajesh Kumar	9.0000	8.9063
Title of Dissertation :		Analyze and Enhance Type C subsystem validation of next generation Intel SoC			
5	32018222	Naveen Kumar Kandula	Kandula Jagannadham	8.0000	8.7031
Title of Dissertation :		Layout versus Schematic for Package			
6	32018224	Pranay Suresh Dorlikar	Suresh Shankar Dorlikar	8.0000	8.9063
Title of Dissertation :		Development of Azure Pipelines for Automation Of Continuous Integration For Post-Silicon Validation			
7	32018240	Vinod Kumar Gill	Suresh Gill	9.0000	8.9688
Title of Dissertation :		Challenges in FC Layout Integration during SoC Timing Coverage			

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 16-August-2022

Chauhan
Prof. In-Charge
(Examinations)

16/08/22
16/08/22
16/08/2022
16/08/22