

## National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations August-2022

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in August-2022 is declared as under:-

**Note: SGPA shown means Pass and "R" means Reappear**

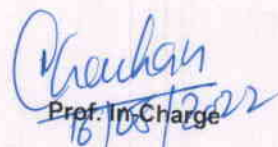
**RL(A) For Result Late (Award)**

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV2D44

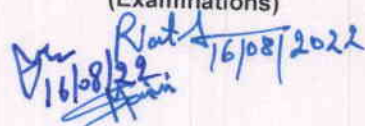
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018103	Divyanshi Bansal	Hanuman Prasad Bansal	6.0000	8.2813
Title of Dissertation :		<b>Area Efficient &amp; Power Saving Periphery off Technique for Memory Chip Design</b>			
2	32018105	Karishma Soni	Ashok Kumar Swarnkar	9.0000	9.0625
Title of Dissertation :		<b>Open-Sourcing a Client BIOS</b>			

All efforts have been made to publish this result after checking the entries properly.  
However, the result can stand revised in case some discrepancy is observed.

Date: 16-August-2022

  
Prof. In-Charge

(Examinations)

  
16/08/2022

# National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations July-2022

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in July-2022 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		
			MSV2D44

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018122	Vindhyali Sood	Keshav Chandra Sood	9.0000	9.4531
Title of Dissertation :		<b>System-on-Chip (SoC) Verification and Silicon Bring Up Infrastructure</b>			
2	32018123	Yaswanth Sai Daida	Rami Reddy	9.0000	9.0938
Title of Dissertation :		<b>Analyze and Enhance Power States IP Validation of Next Generation Intel SoC</b>			

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 16-August-2022

*Phanikan*  
Prof. In Charge  
(Examinations)  
Rat 16/08/2022  
Ar  
16/08/22  
#