

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examinations August-2022

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in August-2022 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018228	Sai Sanjay Vajja	Vajja Rajasheker	9.0000	8.8594

Title of Dissertation : **SMVA For Low Power MV Designs & its Quality Signoff Checks Implementation**

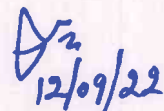
All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 9-September-2022


Prof. In-Charge

(Examinations)

12/09/2022


12/09/22


kvl