

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester **School of VLSI Design and Embedded System (Embedded System Design)** examinations **July-2022**

The Result of the following candidates who appeared in Master of Technology IV Semester **School of VLSI Design and Embedded System (Embedded System Design)** examination of this Institute held in **July-2022** is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

| Sr. No. | Subjects | Code | No. |
|---------|--------------|------|---------|
| 1 | DISSERTATION | | MSV2D44 |

| Sr. No. | Roll No. | Name | Father's Name | SGPA | CGPA |
|---------|----------|--------------------|---------------------|--------|--------|
| 1 | 32018113 | Ritesh Kumar Yadav | Satya Prakash Yadav | 8.0000 | 8.4531 |

Title of Dissertation : **DFT Circuitry's Performance Analysis With and Without EDT(Embedded Deterministic Test)Architecture**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 6-October-2022

Ushah
Associate Dean

(Examinations)

Ritesh 07/10/2022
Dr. A. Z. 06/10/2022
Dr. J. N.