

# National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester **School of VLSI Design and Embedded System (VLSI Design)** examinations **September-2022**

The Result of the following candidates who appeared in Master of Technology IV Semester **School of VLSI Design and Embedded System (VLSI Design)** examination of this Institute held in **September-2022** is declared as under:-

**Note: SGPA shown means Pass and "R" means Reappear**

## RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.		
1	DISSERTATION		MSV1D10		
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018204	Ankit Yadav	Shailendra Singh Yadav	9.0000	9.0625
Title of Dissertation :		<b>Design and Static Verification of Power Intent</b>			
2	32018205	Anupama Raj	Raja Ram	8.0000	8.7656
Title of Dissertation :		<b>Design for Testability (DFT) for Complex ASIC Design</b>			
3	32018208	Brahm Prakash	Ramesh Kumar	8.0000	8.6563
Title of Dissertation :		<b>Characterization and Benchmarking of Dual Port High Speed Memory Compiler for 65nm Technology Node</b>			
4	32018221	Namrata Surendra Shukla	Surendra Shukla	8.0000	8.7031
Title of Dissertation :		<b>Scalable Metric Driven Verification for Completeness using Advance UVM Technique for AXI Protocol</b>			
5	32018223	Pawan Kumar Ray	Jawahar Lal Ray	8.0000	8.1094
Title of Dissertation :		<b>Implementation of Convolutional Neural Networks on FPGA For Image Classification</b>			
6	32018225	Raushan Kumar	Chandra Shekhar Singh	9.0000	8.8906
Title of Dissertation :		<b>Power Aware Signal Integrity Modeling for Serial Link IP using IBIS 5.1</b>			
7	32018227	Ravina Singh Chauhan	Vikram Singh	9.0000	8.6563
Title of Dissertation :		<b>Universal Flash Storage Verification</b>			
8	32018229	Sankeerth Yathati	Y V Sastry	8.0000	8.7344
Title of Dissertation :		<b>Low Power High Performance Bayer Image Compression Scheme</b>			
9	32018235	Sonal Kumar	Kamlesh Kumar	8.0000	8.8438
Title of Dissertation :		<b>Characterization and Benchmarking of Dual Port High Density SRAM Compiler For 65nm Technology Node</b>			

*As*  
10/10/2022

*Rat*  
10/10/2022

*one*

Title of Dissertation : **Analysis of Core Design Method**

**All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.**

**Date: 10-October-2022**

*Chiranjit*  
*13.10.2021*  
Associate Dean

(Examinations)

*Am*  
*10/10/2022*  
*Rat*  
*10/10/2022*