

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester **School of VLSI Design and Embedded System (VLSI Design)** examinations **August-2022**

The Result of the following candidates who appeared in Master of Technology IV Semester **School of VLSI Design and Embedded System (VLSI Design)** examination of this Institute held in **August-2022** is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018217	Manoj Kumar	Sardha Prasad Gupta	9.0000	8.9844
Title of Dissertation :		Validation of CPUID Architecture Flow at Full Chip			
2	32018220	Mohit Tomar	Surendra Tomar	9.0000	9.1563
Title of Dissertation :		Integration of PCIE Transactor in SOC & Comparitive Analysis of FPGA Debugging Tools			
3	32018226	Ravi Teja Chenna	Chenna Raghavaiah	9.0000	8.9688
Title of Dissertation :		Communicating Asynchronous Clock Modules Using AHB Bridge			
4	32018230	Senjuti Sen	Supriya Sen	9.0000	9.0938
Title of Dissertation :		Design of AMBA Bridge For Low Bandwidth Peripherals and Verify Using Formal Verification			
5	32018232	Sheetal Rastogi	Neeraj Rastogi	9.0000	9.2656
Title of Dissertation :		Timing Analysis & Optimum Physical Design Implementation of Digital Design			
6	32018237	Surya Teja Gode	Gode Sreenivasa Prasadbabu	8.0000	8.5625
Title of Dissertation :		Static Checks For RTL Coding Correction			
7	32018238	Swati	Manindra Kumar Tiwari	8.0000	8.9063
Title of Dissertation :		Verification of Safety IP Used In Automotive SOC's Using Universal Verification Methodology			

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 10-October-2022

Chakraborty
17.10.2022
Associate Dean

(Examinations)

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10/10/2022

Rat 10/10/2022
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