

## National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester **School of VLSI Design and Embedded System (Embedded System Design)** examinations **October-2022**

The Result of the following candidates who appeared in Master of Technology IV Semester **School of VLSI Design and Embedded System (Embedded System Design)** examination of this Institute held in **October-2022** is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV2D44

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018110	Pujitha Karamalaputti	K Ramanatha Gupta	9.0000	9.2344

Title of Dissertation : **SPI Slave Controller Design and Implementation**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 7-November-2022

*(Signature)*  
Associate Dean

(Examinations)

*(Signature)* 07/11/22  
*(Signature)* 07/11/22