

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester **School of VLSI Design and Embedded System (Embedded System Design)** examinations **October-2022**

The Result of the following candidates who appeared in Master of Technology IV Semester **School of VLSI Design and Embedded System (Embedded System Design)** examination of this Institute held in **October-2022** is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

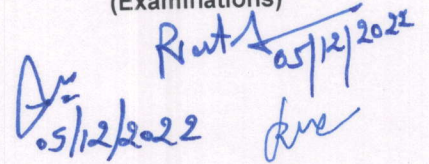
Sr. No.	Subjects	Code	No.		
1	DISSERTATION		MSV2D44		
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018121	Vijendra Bairwa	Gopal Lal Bairwa	8.0000	8.5781
Title of Dissertation :		Design and Implementation of 128-bit Vedic Multiplier Using Different Types of FPGA Architectures			

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 5-December-2022


Associate Dean

(Examinations)


05/12/2022
Rout 05/12/2022
dve