

**NATIONAL INSTITUTE OF TECHNOLOGY
KURUKSHETRA-136119**

No. Acad./Exam. /22-23/ **45**

Date : January 9, 2023

NOTIFICATION

The schedule of Mid Semester Exam-II for B.Tech. 1st Semester in Academic Session 2022-23 is hereby notified in accordance with Academic Calendar for B. Tech 1st semester Notified vide ref. no. Acad./2022/1641 dated 02-11-2022

The Mid Semester Exam-II for B. Tech 1st Semester will be conducted during 16-01-2023 (Monday) to 18-01-2023 (Wednesday) in OFFLINE mode as per the decision of the Senate and letters issued from the office of the Dean(Academic) time to time. The schedule of Mid Semester Exam-II for 1st Semester, B. Tech all programmes is given below: -

(A) Schedule for B. Tech 1st Semester Mid Semester Exam-II

Date & Day	Online Test Slot	Course Code	Course Name#	Branches
16-01-2023 (Monday)	09:30 am to 10:20 am	MAIC11	Differential Calculus and Differential Equations	CE.EE.ME.PI
	10:30 am to 11:20 am			CS.EC.IT
	03:30 pm to 04:20 pm	PHIC11	Physics-I	CE.EE.ME.PI
	04:30 pm to 05:20 pm			CS.EC.IT
17-01-2023 (Tuesday)	09:30 am to 10:20 am	CHIC11	Energy and Environmental Science	CE.EE.ME.PI
	10:30 am to 11:20 am	HSIC11	Communication Skills in English	CS.EC.IT
	12:30 am to 01:20 pm			CS.EC.IT
	03:30 pm to 04:20 pm	CEIR12	Engineering Graphics	ME.PI
	04:30 pm to 05:20 pm			CE.EE
18-01-2023 (Wednesday)	09:30 am to 10:20 am	CSIC11	Problems Solving and Programming Skills-I	CS.IT
	10:30 am to 11:20 am	CSIC13	Problems Solving and Programming Skills-II	CE.EE
	12:30 am to 01:20 pm			EC.ME.PI
	03:30 pm to 04:20 pm	HSIC12	Economics for Engineers	CE.EE.ME.PI
	04:30 pm to 05:20 pm	MEIC11	Engineering Practice	CS.EC.IT

The evaluation scheme for Audit Course, which are qualifying in nature, is already notified vide letter no Acad/ 2023/40 dated 06-01-23 . For more information, please read the letter thoroughly.

(B) Allotment of Rooms

(I) Examination Halls (1 & 2) (II) A-210, A-321 (III) E-101, E-102, E-201, E-202

More vacant rooms from parent department of the course/other departments may also be used in consultation with the respective HoDs as per requirement and Mid Sem Exam time table for other semesters of B. Tech programmes may be prepared accordingly.

(C) Mid Sem Exam-II Duration & Slots

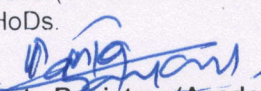
The respective course coordinator may schedule the exam within the time slot allotted as above and complete all the process of exam within the time slot. The course coordinator may schedule Mid Semester Exam-II on any other date in case of unforeseen situation with intimation to the students concerned by email or mobile communication and under intimation to the concerned HoDs. A copy of the same may also be sent to the office of the Dean(Academic).

(D) Invigilation/Supervision duties

Faculty members teaching the course may be assigned duties for Mid Sem Exam conduct/invigilation/supervision etc for these examinations by the respective course coordinators in consultation with the respective HoDs. However, if any department does not have sufficient faculty, the Head of concerned department may take the help of other faculty (Regular and others) from other departments.

(E) Sessional Improvement of courses under old scheme of B. Tech programmes.

The respective course coordinator of the courses of old scheme will also conduct the Mid Sem Exam-II (Sessional Improvement-II) for the students having re-appear in the sessional of the courses for B. Tech - 2021 and previous batches as per scheme and syllabus applicable for them. The cultist for re-appear examinees (old and new scheme) having Email ID and Mobile No. of the examinees has already been sent to the respective HoDs.


Deputy Registrar (Academic)

09/01/23

Copy to:

1. Assistant Registrar to Director for kind information of the Hon'ble Director
2. Heads of all the Departments
3. Associate Dean(Examinations)
4. Notice Boards (Institute/ Hostels through Chief Warden (Hostels))
5. Prof. I/c (CCN) with a request to upload this notification on institute website
6. Dean(Academic)/Associate Dean (Academic) for kind information
7. Sr. Secretary to Registrar for kind information of the Registrar