

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester **School of VLSI Design and Embedded System (Embedded System Design)** examinations **November-2022**

The Result of the following candidates who appeared in Master of Technology IV Semester **School of VLSI Design and Embedded System (Embedded System Design)** examination of this Institute held in **November-2022** is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

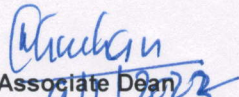
Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV2D44

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32018111	Purushottam Kumar Jha	Sanjay Kumar Jha	8.0000	8.7656

Title of Dissertation : **REAL TIME OBJECT DETECTION AND TRACKING USING SINGLE SHOT MULTIBOX DETECTION (SSD) ALGORITHM**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 7-January-2023


Associate Dean
(Examinations)

