

# 2nd International Conference on Advances in Computing, Communication and Security (I3CS-2023)

01- 03 June 2023

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## Publication:

All accepted and presented Papers will be published in **ITM web of Conferences** archived in **EDP Sciences Digital Library**

## Important Dates

Submission: 31<sup>st</sup> March 2023

Acceptance: 30<sup>th</sup> April 2023

Registration: 10<sup>th</sup> May 2023

## About

The 2nd International Conference on Advances in Computing, Communication and Security (I3CS-2023), aims to bring together leading academicians, scientists, researcher scholars, UG/PG graduates across the globe to exchange and share their research outcomes. It targets to provide a state-of-the-art platform to discuss all aspects (current and future) of advanced computing, communications and security.

## Submission Guidelines

Prospective authors are welcome to submit their full research paper to I3CS-2023 through the Microsoft CMT platform. If you are a new user, you need to sign up firstly. Each full paper should have minimum 5 to maximum 8 pages including all figures, tables, and references. Extra pages will be charged. The authors must submit their paper in the EDP Sciences single column paper template format by following all the mentioned guidelines for authors.

## Template link:

<https://www.itm-conferences.org/for-authors>

## Paper Submission link:

<https://cmt3.research.microsoft.com/2ndI3CS2023/Submission/Index>

## Registration Information

| Category             | Indian Nationals | Foreign Nationals |
|----------------------|------------------|-------------------|
| Author(Student)      | INR 5000         | 100 USD           |
| Author(Academia)     | INR 6000         | 150 USD           |
| Author(Industry/R&D) | INR 8000         | 150 USD           |
| Listener             | INR 3000         | 50 USD            |

## Conference Tracks

### Track 1: Computing

Parallel and Distributed Computing, Cloud Computing, Fog Computing, Soft Computing, Quantum Computing, Scalable Computing, Smart Mobile Computing, Cognitive Computing, Bio-inspired computing, Machine Learning and Artificial Intelligence, Big Data Analysis, Data Mining, Algorithms and Complexity, Architecture and Organization, Internet of Things, Everything and Nano-Things, Human-Computer Interaction, Information Management, Assurance and Security, Intelligent Devices and Environments, Embedded Computing with Applications, Signal Processing, Image and Video Processing, Medical Signal Processing, Speech Recognition, Game Theory, Optimization Algorithms

### Track 2: Communication

Smart Grid Communication, Power Line Communications, 5G & 6G Wireless Communication, Integrating UAVs into 5G systems, Internet of Things, ICT Infrastructure for Smart Cities, RF Systems for Wireless Applications, Wireless Ad-hoc and Sensor Networks, Cooperative Communications, Massive MIMO systems, Information Theory and Coding Systems, Signal and Image processing, Cognitive Radio and Spectrum Management, Software Defined Radio & Networks, Photonics and Optical Communication, Sensors Technology, Network Localization and Navigation, Satellite and Space, Communications, Vehicular, Underground and Underwater Networks, Telehealth

### Track 3: Security

Physical Layer Security, Cryptography and Cryptanalysis, System Security/ Secure OS, Hardware Security, Network Security, Security of cyber-physical systems, Intrusion Detection, Web and Cloud Security, IoT Security, SDN Security, Language based security, Malware and Botnet Detection, Secret Sharing, Social network analytics/ Privacy preservation in social networks, Side Channel Attacks, Computational intelligence techniques in security, Security ontology, models, protocols & policies, Privacy-preserving Security, Data Security and Privacy, Industrial applications of security, Mobile System Security / Android Security, Biometric Security & Privacy, Quantum Cryptography, Smart card security, Security in cashless transactions, Block-chain

### Track 4: VLSI Design & Material Science

Embedded Systems & Processors, System-on-Chip, FPGA, Physical design, Hardware-Software Co-design, Processor architecture, Stochastic Computing, Advanced CMOS technologies and architecture, Low Power IC design, Testing, reliability and fault tolerance systems, RF, analog and mixed-signal design, Digital circuit design for communication and signal processing, Memory computing, EDA algorithms and tools, Nano technology, Material Characterization, System modeling and simulation, Material and energy storage, Quantum materials, Quantum spintronic, Quantum sensors, Energy materials and Semiconductors, Biomedical applications, Modeling and Simulations of Nano devices.

## Organized by:

Department of Electronics & Communication Engineering  
and

Department of Computer Engineering  
National Institute of Technology, Kurukshetra