

## KURUKSHETRA

Kurukshetra is described as DHARMA-KSHETRA, with historical and religious importance. Here, the battle of Mahabharata was fought, and Lord Shree Krishna preached the philosophy of "KARMA" as enshrined in the holy book "Shrimad Bhagwad Gita." It is one of the premier pilgrimage center attracting devotees all round the year. Kurukshetra is very well connected by Rail, Delhi-Ambala section, by Road (NH1, connecting Delhi-Chandigarh-Amritsar-Jammu) and by Air (Delhi 160 km and Chandigarh 80 km). The NIT Kurukshetra campus is situated about 10 km from Pipli, Bus stand located on NH1 and about 6 km from Kurukshetra railway station.



## NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

NIT Kurukshetra, formerly known as Regional Engineering College, Kurukshetra was founded in 1963. It was conferred upon the NIT status, with Deemed University on June 26, 2002. The Institute offers several courses, in various disciplines of B.Tech., M.Tech., MBA and MCA and Ph.D. with an annual intake of about 1500 students. Institute also provides excellent facilities for advanced research in the emerging areas of Engineering, Science, and Technology. The institute has well qualified and dedicated faculty along with supporting staff, laboratories and other infrastructure. The infrastructure is geared to enable the institute to produce technical personnel of high quality.

## SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEM, NITKURUKSHETRA

The school offers M.Tech. and Ph.D. Degrees. The school has two post graduate programs, M.Tech. in VLSI Design; and M.Tech. in Embedded System Design. The school offers Ph.D. in different areas to keep synergy with the evolving innovations and developments in all disciplines of VLSI Design & Embedded Systems. The school is equipped with industry standard EDA/TCAD tools facility for VLSI and Embedded system design. From the different departments such as Electrical, Electronics and Computer Engineering, various faculty members are working in the area of VLSI & embedded Systems.

### RESOURCE PERSONS

Experts from Synopsys/ NIT Kurukshetra/IITs/ NITs. Also, experts may be invited from R&D Organizations.

### COURSE CONVENORS

Prof. R. K. Sharma (NITKKR)  
Mr. Rohit K Ohlaya (Synopsys)

### COURSE COORDINATORS

Dr. M. P. R. Prasad (NITKKR)  
Dr. T N Sasamal (NITKKR)  
Dr Sankalp Singh (Synopsys)

### IMPORTANT DATES

**Last date of Registration:** June 19, 2023  
**Notification of Selection:** June 20, 2023

### CORRESPONDANCE

**Address:**,  
School of VLSI Design and Embedded Systems, NIT  
Kurukshetra – 136119, Haryana, India  
**Email:** mprprasad@nitkk.ac.in  
**Phones:** 09729662574, 8950333079



Celebrating 60 Years of Academic Excellence

## Workshop On ASIC Design and Verification (ASICDV-2023) during June 21-25, 2023



**SYNOPSYS®**

ACADEMIC & RESEARCH  
ALLIANCES

**Jointly organized by**  
**School of VLSI Design and Embedded  
Systems,**  
**National Institute of Technology  
Kurukshetra**  
**Kurukshetra-136119, Haryana, India**  
&

**SYNOPSYS®**  
India

## REGISTRATION FORM

### Workshop on ASIC Design and Verification (ASICDV-2023) June, 21-25, 2023

Name: \_\_\_\_\_  
Title (Dr./Mr./Mrs./Ms.): \_\_\_\_\_  
Sex (M/F): \_\_\_\_\_  
Date of Birth: (dd/mm/yyyy) \_\_\_\_\_  
Designation: \_\_\_\_\_  
Organization: \_\_\_\_\_  
Address for correspondence: \_\_\_\_\_  
\_\_\_\_\_  
Phone: \_\_\_\_\_  
E-mail: \_\_\_\_\_  
Qualification: \_\_\_\_\_  
Category of Registration: \_\_\_\_\_  
Accommodation required\*: Yes / No

#### Payment details:

Draft/Online Details \_\_\_\_\_  
Date: \_\_\_\_\_  
Issuing Bank: \_\_\_\_\_ Amount: \_\_\_\_\_

Signature of applicant (with date)

#### Sponsoring Authority:

Name: \_\_\_\_\_  
Organization: \_\_\_\_\_  
Recommended: \_\_\_\_\_

(Signature of Head of the Department/Section/School/Institute with Seal)

## COURSE OBJECTIVES

VLSI and Embedded Control Systems are very important in space vehicle systems, missile guidance systems, robotic systems, Electronics and Communication Engineering. Also, computer control has become an important and integral part of modern manufacturing and industrial processes. As the world becomes more and more technology-driven, large numbers of increasingly complex systems continue to emerge. These systems must deliver the desired output even in uncertain environments. The need to control and monitor such complex, uncertain systems is imperative.

The main thrust of this course is to present exposure to controller design and its real-time implementation. The applications of various computer control techniques on real-time hardware set up will be shown. Proposed course aims to introduce the fundamentals of modelling, stability analysis and controller design. Further, various advanced techniques for stability analysis and controller design will be discussed. Finally, real-time applications of these techniques on hardware set up will be demonstrated. This course will be helpful to postgraduate students, research scholars, and faculty members. Participants from the following background such as ECE, EEE, Instrumentation, and Mechatronics Engineering are encouraged to attend.

## COURSE CONTENTS

The course aims to address the following issues related to the ASIC Design and verification, but not limited to them.

1. VLSI Applications in Embedded System Design
2. Overview of ASIC design flow
3. Behaviour and functional modelling/RTL Programming
4. Introduction to Synthesis
5. Design for Test
6. Advances in Embedded Control Systems

## WHO SHOULD ATTEND?

Faculty members/ research scholars/ students from academic institutes approved by the AICTE/UGC/MHRD and Scientists/ Engineers working in Private/ Public/ Govt. organizations/ industries etc. can attend the course. The application should be made on the registration form and should accompany registration fee as below:

Participant's category	Registration fee* (in Indian Rupees)
PG Students/ Research Scholars	500/-
Faculty	1000/-
Industry	2000/-

\* **Registration fee is non-refundable. Participants must have valid ID proof of student/ employee from associated organization.**

Participants will be selected on first-come-first served basis. The registration form, complete in all respects, duly forwarded by the Head of the Department/School/Institute, accompanied by Demand Draft/Online details of the requisite amount should reach on or before **June 20, 2023**.

## REGISTRATION FEE PAYMENT

The registration fee is to be paid in advance through an online transaction with the following steps:

1. Go to the website of the State bank of India (<https://www.onlinesbi.com>)
2. Click on State Bank Collect (SB Collect)
3. Agree & Proceed
4. Select State and Type of corporate/Institution: Haryana and Education Institution
5. Educational Institution Name: Director National Institute of Technology Kurukshetra
6. Select payment Category: **ASICDV-2023**. Provide details and submit
7. Select payment methods (Internet Banking, Debit Card, Credit card, etc.).

The information brochure can be downloaded from the institute website, [www.nitkkr.ac.in](http://www.nitkkr.ac.in).

## REGISTRATION FORM

The candidates can fill the Registration Form using the following Google link:

<https://forms.gle/JGWW4tM7ko6NNTaT7>

The soft copy of the completed application forms should be submitted before last date of registration.