

## National Institute of Technology, Kurukshetra

Re-appear Notification for Result of Master of Technology II Semester School of VLSI Design and Embedded System (VLSI Design) Examinations Nov. /Dec. 2022

The Result of the following candidates who appeared in Master of Technology II Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in Nov./Dec. 2022 is declared as under:-

**Note: SGPA shown means Pass and "R" means Reappear**

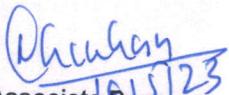
### RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	Modeling of Semiconductor Devices	MSV1C02	
2	Low Power VLSI Design	MSV1C04	
3	Real Time Systems	MSV1E36	
4	VLSI Physical Design	MSV1E40	
5	VLSI Testing and Verification	MSV1E42	
6	VLSI Design Lab-II	MSV1L06	
7	Minor Project	MSV1P08	

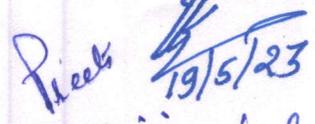
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32118201	Avinash Kumar	Nand Kumar Singh	8.1111	8.2222

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 19 MAY 2023

  
Associate Dean

(Examinations)

  
19/5/23  
