SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEMS
NATIONAL INSTITUTE OF TECHNOLOGY
KURUKSHETRA-136119

No. SVE/2023/262   Dated: 26.08.2023

Subject: Screening of Application for Ph.D Admission in ODD Semester 2023-24

Name	DOB	Father's Name	Full-time/Part-time	GATE/NET/CSIR/ Self-Financed	Eligible/Not Eligible
Ms. Anupriya	7/28/1991	Surender Kumar	Full-time	Self-Financed	Eligible

- Candidate is required to appear in written test on 28.08.2023.
- Visit the Institute website for detailed guideline for written test.

Sharma 26/08/2023
Coordinator
School of VLSI Design & Embedded Systems
26/08/23.

Copy to:
1. Dean (Academic)
2. Prof. I/C (CCN) for arranging the upload of this document on Institute website.