## National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations August, 2023

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in August, 2023 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

## RL(A) For Result Late (Award)

No. Subjects			Code	No.
1 DISSERTATION			MSV2D44	
Roll No.	Name	Father's Name	SGPA	CGPA
32118104	Adrija Chatterjee	Sarbadaman Chatterjee	9.0000	8.8594
e of Dissertation :	System on Chip Para Performance of an In	meter Study and its Applicati tegrated Circuit.	on on Power	and
32118112	Jerubandi Ravi Teja	J Babu	8.0000	8.8281
e of Dissertation :	Surveillance System	for Monitoring Wild Animals.		
32118113	BandaruJahnavi	BandaruSreenivasulu	8.0000	8.7500
e of Dissertation :	Workload Characteria Multi-Core Systems.	zation & Analysis to Optimize	Performance	on
32118114	Surender Kumar	Krishan Kumar	9.0000	8.6406
e of Dissertation :	Verification of Synthe Emulator.	esizable LPDDR5 Memory Mo	del Using ZeE	Bu
32118115	PonnuruPrasanth	PonnuruRaghunath	9.0000	9.0000
e of Dissertation :	Automated Validation Domains of Client SC	n and Debug Methodology for OC in Post-Silicon Validation.	Audio and VI	PU
32118120	Amiya Haldar	SambhuNathHaldar	8.0000	8.1719
e of Dissertation :	Experimentation on S Based FA.	Segmentation Based Multiplie	rs (SAM) with	MUX
32118121	Prashant Kumar	Tej Kumar	8.0000	8.3125
of Dissertation :	APB and SPI Transac	tor		
32118123	TanushDhiman	SubhashDhiman	9.0000	8.4375
	Roll No.  32118104 e of Dissertation:  32118112 e of Dissertation:  32118113 e of Dissertation:  32118114 e of Dissertation:  32118115 e of Dissertation:  32118120 e of Dissertation:  32118121 e of Dissertation:	DISSERTATION  Roll No. Name  32118104 Adrija Chatterjee  System on Chip Para Performance of an In  32118112 Jerubandi Ravi Teja  of Dissertation: Surveillance System  BandaruJahnavi  Workload Characterism Multi-Core Systems.  32118114 Surender Kumar  Verification of Synther Emulator.  32118115 PonnuruPrasanth  Automated Validation Domains of Client SC  32118120 Amiya Haldar  Experimentation on Sased FA.  32118121 Prashant Kumar  APB and SPI Transace	Roll No.  Name  Father's Name  32118104  Adrija Chatterjee  System on Chip Parameter Study and its Application Performance of an Integrated Circuit.  32118112  Jerubandi Ravi Teja  J Babu  Surveillance System for Monitoring Wild Animals.  BandaruJahnavi  BandaruSreenivasulu  Workload Characterization & Analysis to Optimize Multi-Core Systems.  32118114  Surender Kumar  Verification of Synthesizable LPDDR5 Memory Monitoring Monitoring Wild Animals.  PonnuruPrasanth  PonnuruRaghunath  Automated Validation and Debug Methodology for Domains of Client SOC in Post-Silicon Validation.  32118120  Amiya Haldar  SambhuNathHaldar  Experimentation on Segmentation Based Multiplie Based FA.  Prashant Kumar  Tej Kumar  APB and SPI Transactor	DISSERTATION  Roll No.  Name  Father's Name  SGPA  32118104  Adrija Chatterjee  Sarbadaman Chatterjee  9.0000  System on Chip Parameter Study and its Application on Power Performance of an Integrated Circuit.  32118112  Jerubandi Ravi Teja  J Babu  8.0000  Surveillance System for Monitoring Wild Animals.  32118113  BandaruJahnavi  BandaruSreenivasulu  8.0000  Workload Characterization & Analysis to Optimize Performance Multi-Core Systems.  32118114  Surender Kumar  Verification of Synthesizable LPDDR5 Memory Model Using ZeE Emulator.  32118115  PonnuruPrasanth  PonnuruRaghunath  9.0000  Automated Validation and Debug Methodology for Audio and VIDomains of Client SOC in Post-Silicon Validation.  32118120  Amiya Haldar  SambhuNathHaldar  Experimentation on Segmentation Based Multipliers (SAM) with Based FA.  32118121  Prashant Kumar  Tej Kumar  APB and SPI Transactor

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Title of Dissertation :

Hybrid Pill Reminder and Vitals Monitoring System Using IOT

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 1-September-2023

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(Examination)

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