National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations September, 2023

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in September, 2023 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION	MSV1D10	

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32118201	Avinash Kumar	Nand Kumar Singh	8.0000	7.6875

Title of Dissertation : **Performance And MSIX Interrupt Verification In Multi-Gigabit Ethernet IP (MGBE)**

2	32118207	VanshGautam	MukeshGautam	8.0000	8.6406

Title of Dissertation : **Verification of Cache Coherency in a SOC**

3	32118210	Anil Kumar Dubey	Lal Bahadur Dubey	8.0000	8.4531

Title of Dissertation : **Integration of IP that is not IEEE 1687 Standard Compliant**

4	32118211	B Anil Kumar	B GiriBabu	8.0000	8.4531

Title of Dissertation : **Study of Power Estimation and Power Reduction Techniques For a Complete SoC**

5	32118212	BurubuPrasanna Kumar	Burubu Ramesh Babu	8.0000	8.7344

Title of Dissertation : **Automation of Formal Verification Process and Extraction of FEV Indicators**

6	32118213	Mani Jain	Akhilesh Jain	8.0000	8.5000

Title of Dissertation : **Verification of AXI Protocol in IP Through Functional Coverage**

7	32118217	BhumaMounika	BhumaSankara Reddy	9.0000	8.7656

Title of Dissertation : **Adaptive Placement And Routing Strategies For Complex SoC Designs**

8	32118221	Shikhar Gupta	Umesh Kumar Gupta	8.0000	8.6875

1 0 OCT 2023

Title of Dissertation :	**Design and Implementation of Low Power Chip for Congestion and Timing Optimization in High-Performance Systems**			
9 32118222	Arpit Sharma	Kulbhooshan Sharma	9.0000	8.9844

Title of Dissertation : **Front-end Verification and Synthesis of SOC RTL**

10 32118223	MadabattulaBhargav Vinay	MadabattulaVenkataSatyanarayana	8.0000	8.9531

Title of Dissertation : **Accelerating Code Coverage for Digital Circuits Using Formal Verification and Unreachability Analysis**

11 32118224	VikasVerma	Ram Sudhi	9.0000	8.7031

Title of Dissertation : **Correlation Study on Advanced Process Technology for Timing, DRC and RC Parameters**

12 32118226	AbhinavSahu	Ram BabuSahu	8.0000	8.6875

Title of Dissertation : **Power Performance and Area Optimization of High-Speed Circuits Using FSDB Based Technique**

13 32118235	Deepak Kumar	Ramawatar Mandal	9.0000	8.3906

Title of Dissertation : **Low Power Digital Circuit Design and Power Analysis Flow Used in Industry**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

10 OCT 2023

Faculty In-Charge

(Examinations)