National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations September, 2023

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in September, 2023 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. M	No. Subjects			Code	No.	
1 DISSERTATION				М	MSV1D10	
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA	
1	32118208	Amit Pant	Diwakar Pant	9.0000	8.7344	
Title	of Dissertation :	Analysis of PDN Factor Timing Analysis	ors in Real Environment Chip	s by Using S	tatic	
2	32118209	Anand Kumar Pandey	Umesh Chandra Pandey	9.0000	8.8125	
Title	of Dissertation :	Verification of System	on Chip Model Using Emula	tion		
3	32118220	Saurav Kumar	Om Prakash Singh	9.0000	7.9688	
Title	e of Dissertation :	Bus Certification of Lo Techniques	ow Latency Bridge Using For	mal Verificat	ion	
4	32118229	UppalaVeera Ganesh	UppalaSambasiva Rao	9.0000	9.1250	
Title	e of Dissertation :	Automation of Quality	Checks in Placement and R	oute Sub Flo	w	
5	32118237	Ashish	Dinesh Kumar Mahto	9.0000	8.9844	
Title	e of Dissertation : 🤇	Analysis of Low Powe Design	er Optimization Methods for A	Advanced SC	C	

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 2 3 OCT 2023

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