National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations October, 2023

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in October, 2023 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. M	No. Subjects			Code	No.
1 DISSERTATION				MSV2D44	
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32118116	DasariDhanishta Sai	DasariVenkataramaiah	6.0000	7.9531
Title	of Dissertation :	Firmware, Acceptance in Post Silicon Enviro	ce and Validation of OTP Con onment	troller on So	C/CPU
2	32118117	Vikas Yadav	Virender Singh	6.0000	8.1406
Title	e of Dissertation :	 Toggle Coverage Too Designs 	ol For Structural/Functional V	erification of	

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 2 3 NOV 2023

Faculty In-Charge (Examinations)