

Acad./Inward/29/1483
Dated: 12/4/24

OFFICE OF THE DEAN (RESEARCH & CONSULTANCY)
NATIONAL INSTITUTE OF TECHNOLOGY
KURUKSHETRA-136119

No. Dean (R&C)/24/75

Dated: 12.4.2024

Subject: Allotment of Full time and Part Time Seats under Visvesvaraya PhD Scheme

With reference to implementation orders No. PhD-02/2024-03/20 dated 05.03.2024 and PhD-PT02/2024-02/16 dated 11.03.2024, the institute has been allotted 3 full-time and 2 part-time PhD scholarships under Visvesvaraya Ph.D. Scheme for Electronics & IT Phase-II (Details enclosed).

In view of the above, you are requested to advertise additional Ph.D scholarships as per the following details:

Title of Project	Funding agency	Department	No of Slots		Area of specialization
			Full Time	Part Time	
Visvesvaraya PhD Scheme for Electronics and IT: Phase-II	MeitY Government of India	Computer Engg	1	1	ESDM/IT/IT enabled services
		Electronics and Communication Engg.	1	-	
		School of VLSI Design and Embedded Systems	1	1	

The full-time PhD scholars will be provided Rs. 38,750/- (for the first two years) and Rs. 43,750/- per month for 3rd-5th year. It has the provision of HRA as well as contingency grant of Rs. 1.20 lacs/student each year. Total duration of scholarship is 60 months.

The part-time PhD candidates are eligible to receive a one-time incentive of Rs. 3.00 Lakhs on successful completion of PhD and other required formalities.

Dean (Academic)

Phubans
12/04/24

Supdt (A)/Ph.D.

(Dr. N.K. Singh)

Nodal Officer

Visvesvaraya Ph.D. Scheme

Digital India Corporation

(A Section 8 Company)

Ministry of Electronics and Information Technology,
Govt. of India



डिजिटल इंडिया कॉर्पोरेशन

(धारा 8 कंपनी)

इलेक्ट्रॉनिक्स और सूचना प्रौद्योगिकी मंत्रालय,
भारत सरकार

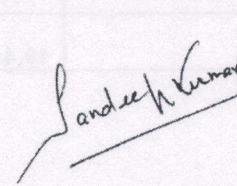
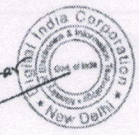
PhD Cell

Ref.: Implementation Order No: PhD-02/2024-03/20

Date: Mar 05, 2024

Subject: Implementation Order for the execution of 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at National Institute of Technology, Kurukshetra

1. The undersigned is directed to convey the approval of Competent Authority for allotment of 3 Full Time PhD seats for implementation of 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at National Institute of Technology, Kurukshetra for Academic Year 2024-25.
2. The estimated budget outlay for 3 Full Time PhD candidates of your institution under Visvesvaraya PhD Scheme is Rs. 1,02,91,200/- with a duration of 60 months or PhD completion of the enrolled candidates, whichever is earlier. Details are in the Annexure (attached).
3. Before proceeding with the enrollment of candidate(s) on the allocated seat(s), the institution needs to accept the 'Terms and Conditions' including 'Standard Operating Procedure' (attached). The acceptance should be duly signed & stamped, by the head of the institution of National Institute of Technology, Kurukshetra. Soft copies of the signed and stamped documents should be uploaded on the PhD Scheme portal and sent to phd-scheme@digitalindia.gov.in latest by the end of April 2024. The original documents need to be sent to the undersigned at the following address- PhD Cell, Room No. 2084, Electronics Niketan, 6, CGO Complex, Lodhi Road, New Delhi-110003.
4. The enrolment of PhD candidates followed by their verification & other formalities as per the 'T&C' should be completed by Sep 2024 of the Academic Year 2024-25.
5. The candidates, enrolled & verified by the institution would be eligible to receive monthly fellowship & "Reimbursement of Rent" which will be transferred directly to the bank accounts of the PhD candidate through DBT. The institution will submit annual performance report for each candidate enrolled under the scheme. In case of non- performance of any candidate, change in fellowship due to absence etc., change in amount of RoR due to change in accommodation, or drop-out etc.; the institution shall intimate PhD Cell, DIC regarding the same immediately. In case no such intimation is received, PhD Cell, DIC will continue to transfer the fellowship & RoR directly to the bank accounts of PhD candidates and recovery of the excess amount would be the responsibility of the institution. For more details, please refer to the Guidelines & SoP as attached.
6. The PhD Cell team would be interacting with the nodal officer of your institution for routine matters, however, in some cases the intervention of the head of the institution would be required for which the PhD Cell team will keep updating the institution through different channels e.g. phd-scheme@digitalindia.gov.in or telephonically.

Sandeep Bansal
(In-charge, PhD Cell, DIC)

To,
Prof. B. V. Ramana Reddy
Director
National Institute of Technology, Kurukshetra

Copy to,

1. Dr. Nirmal Kant Singh, Nodal Officer, National Institute of Technology, Kurukshetra
2. Shri Abhishek Singh, IAS, AS-MeitY; MD&CEO-DIC
3. Shri Sivadas, Chief Manager Finance, Digital India Corporation

PhD Cell, Room No 2084, Ministry of Electronics & Information Technology (MeitY),
Electronics Niketan, 6, CGO Complex Lodhi Road, New Delhi-110003 (India)

Tel.: +91 (11)24301284, 24301285

Website: digitalindiaincorporation.in

Digital India Corporation

Annexure to the Implementation order no.: PhD-02/2024-03/20 dated Mar 05, 2024

Sr. No.	Name of the Scheme/ Project	'Visvesvaraya PhD Scheme for Electronics and IT: Phase II at National Institute of Technology, Kurukshetra
1	Objective of the Scheme/Project	Successful completion of PhD, by the candidates to be enrolled against the PhD seats, as allotted in this Implementation Order, following the guidelines of the scheme and periodic directions from Digital India Corporation, MeitY.
2	Area of Coverage	ESDM and IT/ ITES.
3	Name of Implementing Agency	National Institute of Technology, Kurukshetra
4	Location/ Address	National Institute of Technology Kurukshetra
5	No. of Full Time PhD Candidates	3
6	Total Scheme/ Project Duration	60 months or PhD completion of the enrolled Candidate(s), whichever is earlier

Total Scheme/ Project Outlay:

Amount in Rs.

Budget Head	1st Yr	2nd Yr	3rd Yr	4th Yr	5th Yr	Total
Full Time fellowship @Rs.38750/- per years, and Rs.43750/-per month in 3rd, 4th and 5th years of PhD	13,95,000	13,95,000	15,75,000	15,75,000	15,75,000	75,15,000
Reimbursement of Rent (RoR) (Class Z @8%)	1,11,600	1,11,600	1,26,000	1,26,000	1,26,000	6,01,200
Institutional Overhead	75,000	75,000	75,000	75,000	75,000	3,75,000
Research Contingency Grant	3,60,000	3,60,000	3,60,000	3,60,000	3,60,000	18,00,000
Total	19,41,600	19,41,600	21,36,000	21,36,000	21,36,000	1,02,91,200

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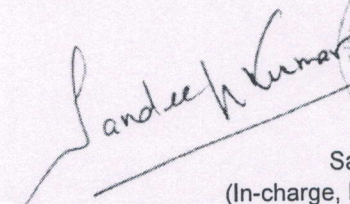
PhD Cell

Ref.: Implementation Order No: PhD-PT02/2024-02/16

Date: Mar 11, 2024

Subject: Implementation Order for the execution of 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at National Institute of Technology, Kurukshetra

1. The undersigned is directed to convey the approval of Competent Authority for allotment of 2 Part Time PhD Seat under 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at National Institute of Technology, Kurukshetra for Academic Year 2024-25.
2. The estimated budget outlay for 2 Part Time PhD candidate(s) of your institution under Visvesvaraya PhD Scheme is Rs. 6.00 Lakhs. The Part-time PhD candidate is eligible to receive a One-time incentive of Rs. 3.00 Lakhs, through DBT in his/her bank account, on successful completion of PhD and required formalities with the institution and the scheme; such as submission of requisite completion documents, claims, etc. through the institution to DIC.
3. Before proceeding with the enrollment of candidate(s) on the seat(s), the institution needs to accept the 'Terms and Conditions' including 'Standard Operating Procedure' (attached). The acceptance should be duly signed & stamped, by the head of the institution. Soft copies of the signed and stamped documents should be uploaded on the PhD Scheme portal and sent to phd-scheme@digitalindia.gov.in latest by the end of Apr 2024. The original documents need to be sent to the undersigned at the following address- PhD Cell, Room No. 2084, Electronics Niketan, 6, CGO Complex, Lodhi Road, New Delhi-110003.
4. The institution will submit annual performance report for the candidate(s) enrolled under the scheme. In case of non-performance of the candidate(s), drop-out etc. the institution shall intimate PhD Cell, DIC regarding the same immediately. For more details, please refer to the Guidelines & SoP as attached.
5. The PhD Cell team would be interacting with the nodal officer of your institution for routine matters, however, in some cases the intervention of the head of the institution would be required for which the PhD Cell team will keep updating the institution through different channels e.g. phd-scheme@digitalindia.gov.in or telephonically.


Sandeep Bansal
(In-charge, PhD Cell, DIC)

To,
Prof. B.V. Ramana Reddy
Director
National Institute of Technology, Kurukshetra

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