SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEMS
NATIONAL INSTITUTE OF TECHNOLOGY KURUKSHETRA

No. SVE/2024/205   **Date: 20.06.2024**

<u>**Subject:**</u> Admission to Ph.D. Program in School of VLSI Design and Embedded
Systems (SVE) of NIT Kurukshetra in the odd semester of the academic year 2024-25

Schedule of Examination:

1. Reporting to office of SVE Dept., NIT Kurukshetra: **09:30 AM; 24.06.2024**
2. Written Test (Paper-1): **10:00 am ; 24.06.2024**
3. Written Test (Paper-II): **12:00pm (afternoon) ; 24.06.2024**
4. Interview for the candidates qualified in the written tests: **03:30 PM; 24.06.2024**

Sharma
Chairman SRC
20/06/2024

Prof. I/C, CCN to upload on the website of the institute (urgent)

Copy to:
   1. Dean (Acad.)