

National Institute of Technology, Kurukshetra

Re-appear Notification for Result of Master of Technology I Semester School of VLSI Design and Embedded System (VLSI Design) Examinations May/June 2024

The Result of the following candidates who appeared in Master of Technology I Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in May/June 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	Digital IC Design	MSV1C01	
2	Analog IC Design	MSV1C03	
3	Hardware Description Languages and FPGA Based Design	MSV1C05	
4	Embedded System Fundamentals	MSV1E31	
5	Solid State Devices	MSV1E43	
6	VLSI Design Lab-I	MSV1L07	
7	Minor Project	MSV1P09	

Sr. No.	Roll No.	Name	Father's Name	SGPA
1	323108206	Rahul Patel	Late Asharam Patel	7.6667
2	323108211	Abhishek Guglia	Mukesh Kumar	7.3889
3	323108215	Madhu	Moti Ram	9.5000
4	323408205	Ujjawal Kulshreshtha	Ajay Narain Kulshreshtha	6.8889
5	323408209	Mansi Tripathi	Bibhas Tripathi	5.7778
6	323408211	Pooja	Kartar Singh	6.0556
7	323408212	Kalagoni Naga Sai Santosh	K Sreenivas Rao	7.1667

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

26 JUL 2024

Faculty In Charge

(Examinations)

P. K. Sharma
26/07/24
Shini

26/7/24