

NATIONAL INSTITUTE OF TECHNOLOGY, KURUKSHETRA

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examinations July, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in July, 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)


Sr. No.	Subjects	Code No.
1	DISSERTATION	MSV2D44

Sr. No.	Roll No.	Name	F_Name	SGPA	CGPA
1	32219119	Abhishek Chanchal	Manoj Kumar	9.0000	8.8125

Title of Dissertation : **High Speed Area Efficient 32 Bit BCD Adder**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date **29 JUL 2024**


Faculty in Charge
(Examinations)

Recd: Anay 29/7/24 *29/7/24*
Haini