

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations September, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) examination of this Institute held in September, 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

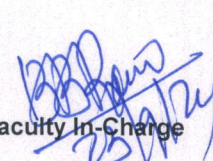
Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV2D44

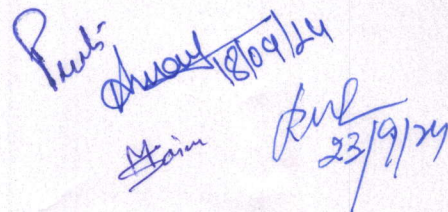
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219110	Veereju Sanath Kalyan	Veereju Venkata Ramana	8.0000	8.9531

Title of Dissertation **Implementation of an SOC Design Intended for Congestion, Timing & Power Optimization**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 23 SEP 2024


Faculty In-Charge
(Examinations)


Head
23/9/24