National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations September, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in September, 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219206	Abhinav Rajyan	Maheshwar Prasad	8.0000	8.8125
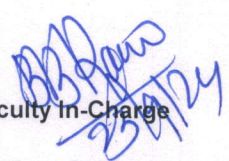
Title of Dissertation : **RV321-Compliant RISC-V Processor Core Design with Multi-Unit Architecture**
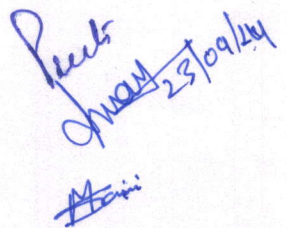
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
2	32219207	Avinash Ganta	Ganta Satyanarayana	9.0000	8.8594

Title of Dissertation : **Synergistic Optimization of Leakage Power, Timing, and Area for Improved Design Performance**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 2 3 SEP 2024

Faculty In-Charge

(Examinations)