

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations August, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in August, 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
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1	32219211	Anirban Sarkar	Tapash Sarkar	9.0000	9.0313
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Title of Dissertation : **Characterization of an MCU Chip for 13C Protocol and its Testing Automation**

2	32219212	Gandla Nitish	Gandla Ajay Kumar	9.0000	9.0469
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Title of Dissertation : **Addressing Design Challenges Using Logic Equivalence Check and Low Power Verification on SoC Design**

3	32219227	Vishal Raj	Rakesh Kumar	9.0000	8.4375
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Title of Dissertation : **Formal Verification of Network Port**

4	32219228	Prashant Kumar	Ram Abhilash	9.0000	8.1719
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Title of Dissertation : **PCIe Technology: Roadmap and Its Validation Strategies**

5	32219229	Abhimanyu Kumar	Dayanand Prasad	8.0000	8.5000
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Title of Dissertation : **SoC Level Verification of Timer IP (Including BASIC, PWM, LITE and LPTIMER)**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 23 SEP 2024

Faculty In-Charge

(Examinations)

Prashant
M. Saini
18/09/24 23/9/24