

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations October, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in October, 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code No.
1	DISSERTATION	MSV2D44


Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219106	Nigu Kumari	Harender Prasad	9.0000	8.8594
2	32219108	Billakota Surya Satya Sai Praneeth	Billakota Mallikarjuna Rao	9.0000	8.5000

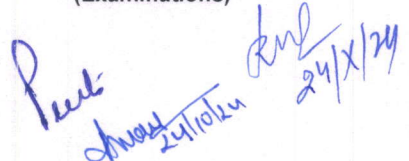
Title of Dissertation : **High Volume Functional Validation**

Title of Dissertation : **Optimizing Dynamic Shift Register RTL for Efficient Repair Data Transfer**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 25 OCT 2024


Faculty in Charge
(Examinations)


24/10/24