

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations September, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in September, 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219222	Sanjeev Kumar	Shatrughan Ram	9.0000	8.6094

Title of Dissertation : **Methodology and Validation of Power Management Flows**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date:

25 OCT 2024

Faculty In-Charge

(Examinations)

Handwritten signatures and dates:
Pankaj
Shree 24/10/24
M. Amin
Kul 24/10/24