

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations September, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in September, 2024 is declared as under:-


Note: SGPA shown means Pass and "R" means Reappear


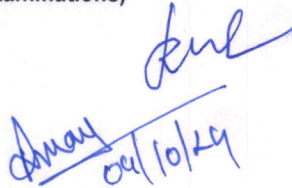
RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.		
1	DISSERTATION		MSV1D10		
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219223	Bollu . Avinash	Bollu Srinivasa Rao	9.0000	8.2969
Title of Dissertation Leveraging Formal Methodologies for Connectivity, Coverage and Data Integrity Verification					
2	32219239	Rahul Rana	Parshu Ram Rana	9.0000	8.5625
Title of Dissertation Design and Performance Evaluation of Low Power Multiplier for FPGA Based DSP Systems					

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 09 OCT 2024


Faculty In-Charge
(Examinations)



09/10/24
