

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations September, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) examination of this Institute held in September, 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
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1	32219231	Rajeev Kumar Chauhan	Chandr Pal Singh	9.0000	8.5938
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Title of Dissertation **Secure Identity Verification System Interfaced with Memory for Data Storage and Recovery**

2	32219237	Archit Sundriyal	Naresh Sundriyal	9.0000	9.3438
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Title of Dissertation **Quality Checks for CDC and Constraints of High-Speed Sub-System for FPGA Tile Design**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 04 OCT 2024

Faculty In Charge

(Examinations)

Praveen
03/10/24

dev
03/10/24