

National Institute of Technology, Kurukshetra

Notification for Result of Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examinations October, 2024

The Result of the following candidates who appeared in Master of Technology IV Semester School of VLSI Design and Embedded System (VLSI Design) Examination of this Institute held in October, 2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

RL(A) For Result Late (Award)


Sr. No.	Subjects	Code	No.
1	DISSERTATION		MSV1D10

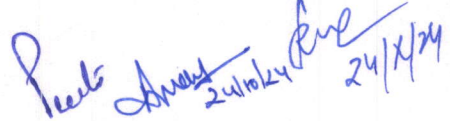
Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	32219238	Samarth Verma	Veer Sen	9.0000	8.6094

Title of Dissertation : **Signoff Closure of Arm Cortex M85**

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 25 OCT 2024


Faculty In-Charge
(Examinations)


24/10/24 24/10/24