

**SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEMS  
NATIONAL INSTITUTE OF TECHNOLOGY  
KURUKSHETRA**

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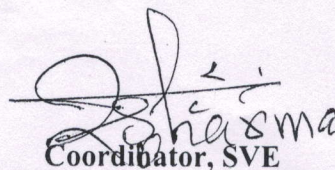
No. SVE/2024/502

Dated: 29.11.2024

**Subject: Schedule for Ph.D admission written test and presentation for Even Sem. Of  
AY 2024-25**

Following is the Schedule for Ph.D admission written test and presentation for Even Sem. Of  
AY 2024-25:

Type of test	Date	Time	Venue
Paper-I	02/12/2024	10:30 AM	School of VLSI Design & Embedded Systems
Paper-II		11:45 AM	
Presentation/Interview		01:30 PM	

  
Anshu Ma  
Coordinator, SVE  
29/11/2024

**Copy to:**

1. Prof. I/c (CCN) for kind information and with a request to please get this notice uploaded on the Institute website.
2. HOD (ECE), Kindly ensure that there is no conflict between the Ph.D. admission schedules