

**NATIONAL INSTITUTE OF TECHNOLOGY
KURUKSHETRA-136119**

Scrutiny Result for Ph.D. admission in Even Semester of the AY 2024-25

Name of the Department /School: SCHOOL OF VLSI DESIGN & EMBEDDED SYSTEMS

Sr. No.	Application No.	Name of the Candidate	Father's Name	Candidate Category	Admission Category	Part Time / Full Time	Eligible / Not Eligible	Exempted from the written test (Yes/No)	Remarks
1	6	alka parihaar	arvind singh parihaar	General	General	Part-Time	Eligible (in Full Time only)	No	1. Valid NOC not attached for part-time (experience less than 1 Year) 2. PG in Digital Systems 3. Research interest: VLSI DESIGN and EMBEDDED SYSTEM
2	48	Himanshu	Prem Datt	General	General	Full-Time	Eligible	Yes	1. PG in VLSI design 2. Research interest: VLSI Design, Semiconductor Devices, Sensors
3	114	NITISH VASHISHTH	SATYNARAYAN VASHISHTH	General	General	Full-Time	Eligible	Yes	1. PG in VLSI design 2. Research interest: VLSI design
4	119	Pallvi Gupta	Ramesh Chand Gupta	General	General	Full-Time	Eligible	No	1. PG in Microelectronics & VLSI design 2. Research interest: VLSI, AI/ML

Prof. I/C (CCN) with a request to get it uploaded on the Institute Website (Urgent)

Copy to: **Dean (Academic) for information**


D.K. Chaurasia/HoD
29/11/2024