

National Institute of Technology, Kurukshetra

Re-appear Notification for Result of Master of Technology II Semester School of VLSI Design and Embedded System (Embedded System Design) Examinations Nov./Dec.2024

The Result of the following candidates who appeared in Master of Technology II Semester School of VLSI Design and Embedded System (Embedded System Design) Examination of this Institute held in Nov./Dec.2024 is declared as under:-

Note: SGPA shown means Pass and "R" means Reappear

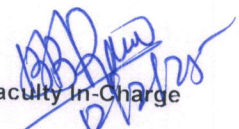
RL(A) For Result Late (Award)

Sr. No.	Subjects	Code	No.
1	Real Time Systems	MSV2C22	
2	Hardware Software Co-design	MSV2C24	
3	Low Power VLSI Design	MSV2E42	
4	Embedded Application Lab	MSV2L26	
5	Minor Project	MSV2P28	
6	Digital Signal processing with FPGAs	MSV2E44	
7	Computer Based Control Systems	MSV2E40	

Sr. No.	Roll No.	Name	Father's Name	SGPA	CGPA
1	323108109	Kundan Kumar	Shyambabu Sah	8.0000	7.8333
2	323108122	Karshni Kant Pathak	Gopi Kant Pathak	7.5000	7.8889

All efforts have been made to publish this result after checking the entries properly. However, the result can stand revised in case some discrepancy is observed.

Date: 12 FEB 2025


Faculty in-Charge
(Examinations)

